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ISSUE CLASSIFICATION	
Class	Subclass

PATENT NUMBER

U.S. **UTILITY** Patent Application

MT

O.I.P.E.

PATENT DATE

SCANNED

HC

Q.A

APPLICATION NO.
09/997730

CONT/PRIOR
F

CLASS
428

SUBCLASS

ART UNIT 1772

EXAMINER

APPLICANTS

[illegible]

TITLE

Plasma, and Semiconductors and Printed Wiring Board Testers

P10-2040
12/99

ISSUING CLASSIFICATION

[illegible]

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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS <div> <div>Sheets Drwg.</div> <div>Figs. Drwg.</div> <div>Print Fig.</div> </div>		CLAIMS ALLOWED	
			<div>Total Claims</div>	<div>Print Claim for O.G.</div>
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ <div>_____</div> <div>_____</div>	<div>_____ (Assistant Examiner) _____ (Date)</div> <div>_____ (Primary Examiner) _____ (Date)</div>		NOTICE OF ALLOWANCE MAILED <div>_____</div>	
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